

A MINI REVIEW ON ELECTRON TRANSPORT LAYERS (ETLS) FOR PEROVSKITE SOLAR CELLS

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ABSTRACT

Perovskite solar cells (PSCs) have demonstrated rapid efficiency and advanced photovoltaic technology in the last decade. Ideally, the absorber layer in PSCs is sandwiched between a highly conductive electron transport layer (ETL) and a highly stable hole transport layer (HTL). The presence of these two layers significantly impacts the performance of PSCs. This paper will review the common materials used for ETL, various deposition techniques to grow ETL, their advantages and disadvantages, and recent advances of TiO₂ films as an ETL including their challenges and future perspective. In this context, TiO₂ nanostructure due to their low cost, non-toxicity, and ease of manufacture, are the most prominent photocatalyst choices. Finally, this review shows how doped TiO₂ can be a beneficial approach to improve the PSC's performance and stability.

1.0 ROLE OF ETL IN PSCs

Solar energy is the energy obtained by capturing heat and light from the Sun. The sun is also considered as an infinite energy source and can be accessed nearly everywhere, making it the most reliable energy source. Electrical devices that convert light energy or photon from the sun to electrical energy through photovoltaic (PV) effect are called solar cells. The first generations of solar cells were based on crystallite silicon but, it required very complex fabrication process and high cost. The second generation of solar cells is based on thin films. Typical materials used for this kind of solar cell are cadmium telluride (CdTe) and copper-indium-gallium-diselenide (CIGS). The photovoltaic technologies for third generation are based on more recent chemical compounds and includes several thin-film technologies often described as emerging photovoltaics. In addition, technologies using nanocrystalline films based on new materials and cutting-edge technologies. Perovskite solar cells (PSCs) is one kind of the third generation of solar cells that consists of perovskite structure compound as an active layer with speedy increasing in power conversion efficiency (PCE) from 13% to certified 26% during the past 10 years [1-3]. Perovskite has an absorption coefficient ($\sim 10^6 \text{ cm}^{-1}$) that is significantly higher than crystalline silicon. The conventional device structure of PSCs consisted of transparent conducting electrode (TCO) layer of fluorine-doped tin oxide (FTO) as bottom electrode, an electron transport layer (ETL), perovskite absorption layer, a hole-transport layer (HTL) and top electrode as shown in Figure 1. There are two types of PSCs device architecture according to the arrangement of the precedent ETL or HTL that attributed to the regular n-type – intrinsic – p-type (n-i-p) structure or inverted p-type – intrinsic – n-type (p-i-n) structure, respectively depend on which transport material on the exterior portion of perovskite will encounter light first. Figure 1 shows n-i-p and p-i-n device structure. In n-i-p structure, the ETL is deposited first, followed by perovskite active layer and HTL so that ETL encounters sunlight first. In p-i-n structure, the HTL is deposited first followed by perovskite active layer and ETL, where in this case, the light falls on the HTL first.

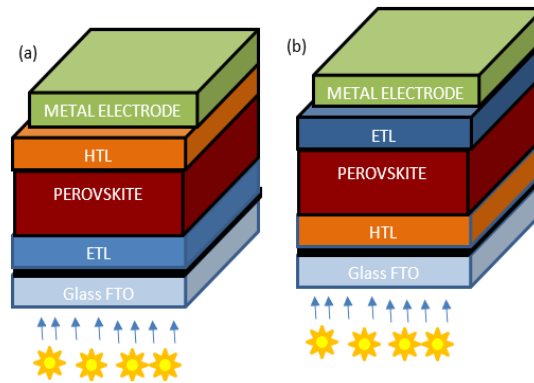


Figure 1. (a) n-i-p and (b) p-i-n devices structure

ETL plays a crucial role in extracting and transporting photogenerated electron carriers and serves as a hole-blocking layer by suppressing charge recombination as one of the most important components for photovoltaic devices [4]. The physical properties of the ETL, including charge mobility, energy level alignment, defective states, morphology, and related interfacial properties, are significant for the photovoltaic performance [5]. To achieve a high-performance PSCs, the presence of an ETL is required to meet the following criteria. Firstly, the ETL must have good energy-level alignment, so the charge transfer and hole blocking can be efficient. Next, have the high electron mobility to enable fast electron transport within the ETL. Besides that, ETL materials must have a wide band gap to ensure high transparency and allow lighter to pass through it so that light absorption by the perovskite materials can be maximized. Other than that, materials must have compatible energy levels, where the ETL must have a compatible conduction band minimum and valence band maximum in relation to the energy levels of the perovskite light harvester to facilitate electron transfer and block hole transport from the perovskite layer. Lastly, easy processing deposition techniques such as solution processed, is to ensure uniform and high-quality film formation and enable mass production [6]. Even though various groups claimed to have a PCE of more than 13% for perovskite devices without the ETL [7-8], the formation of ETL in PSCs is still dominant in terms of efficiency and stability of the devices. Juarez-Perez et al. compared the device performance of the complete PSCs with the ETL-free device, they observed that the absence of ETL had a significant impact on the electrical output parameter and device efficiency [9].

By removing the TiO_2 layer compact, it has little effect on the open circuit potential, V_{oc} , but affects deleteriously the short circuit photocurrent, J_{sc} , and the fill factor, FF , nearly having the photoconversion efficiency, η , in comparison to the complete device. Zhang et al. also reported a reduction of efficiency from 14.2% to 2.7% upon the removal of the ETL [10]. They observe that the ETL free planar cells have almost zero stabilized power output and there is a short-lived effect responsible for hysteresis in these devices, which then disappears during the stabilized power scan. It was also reported that the PCE of the ETL free device could reach up to 14.1%, but the complete cells exhibited superior efficiency of 16.1% [8]. The efficiency improvement was due to the enhancements of FF and J_{sc} . The chlorine may passivate the FTO/perovskite interface, and the passivation seems to be effective with the presence of TiO_2 as ETL.

2.0 ORGANIC AND INORGANIC MATERIALS FOR ETL

Organic material such as fullerene (C_{60}), graphene and its derivative have been widely used as ETL in PSCs [12-13]. The advantages of organic ETLs is that they can be easily prepared by solution process and the devices showed good performance, but their environmental, thermal, and photostability is quite concerning for the device's long-term stability. Fullerene-based molecules, is an organic material that could minimize the density of defects at the perovskite/ETL interface. Pandey et al. had successfully deposited a thin layer of C_{60} using spin coating and observed that C_{60} has bandgap around 1.7 eV [13]. Ke et al. reported that insertion of C_{60} as an ETL, produced by vacuum process with thickness of 7.5 nm could enhance the short circuit current density (J_{sc}) and fill factor (FF) values of PSCs to the maximum, leading to a PCE of 15.1% [8]. Furthermore, the employment of C_{60} as an ETL helped to avoid trapped charge induced degradation in the presence of moisture. Study from Yoon et al. using a room temperature vacuum-processed C_{60} as an ETL has demonstrated a PCE of 19.1% [14]. The highly homogeneous, uniform, and dense ETL with a thickness of 35 nm was found to not only passivate the grain boundaries and surfaces of the perovskite layer, but also enhanced the charge transport property. The results of the

structure of conventional of (Methyl-ammonium lead iodide) MAPI PSCs with C60 as ETL is represented in Figure 2.

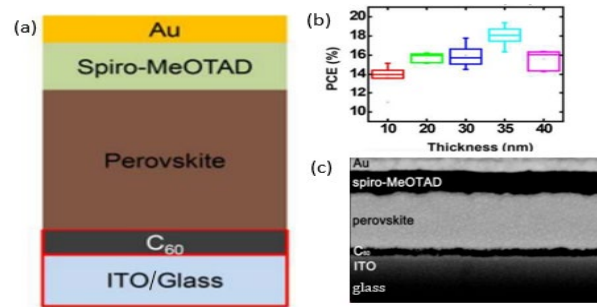


Figure 2. (a) The structures of conventional perovskite solar cells with C60 layer as ETL, (b) PCE comparison with different C60 thickness and (c) cross sectional FESEM image of a flexible MAPbI₃ perovskite solar cell

Other organic materials that have been reported as ETL are graphene and reduced graphene. To improve the PSCs electron efficiency, graphene and reduced graphene oxide, are typically doped with other inorganic materials to create nanocomposite ETLs. Biccari et al. investigated the effects of different graphene-based ETLs in sensitized MAPI photoelectrodes [12]. They observed that graphene based ETLs significantly improve both the carriers collection and the crystalline quality of the active material. The FESEM image in Figure 3 also demonstrated the enhancement of crystal size and shape regularity after the addition of graphene. From this work, the PSCs produced by graphene-doped TiO₂, showed an improvement in PCE with 16% efficiency compared to undoped TiO₂ (14.6% PCE). Further work by Agresti et al. proposed a new PSCs structure by including lithium neutralized graphene oxide (GO-Li) as the ETL on top of the mesoporous TiO₂ substrate and this result showed an improvement of short circuit current and fill factor [15]. The main effect of GO-Li is its improving the electron injection from the sensitizer to the m-TiO₂ layer by remarkably increasing the J_{SC} and demonstrated an impressive PCE of 12% over the performance of the reference sample (9.6%). Study done by Chandrasekhar et al. has been reported in 2017 using Zinc Oxide (ZnO) and doped ZnO with different concentration of graphene as ETL [16]. Although, the SEM images show the similar morphology between pristine ZnO and graphene doped ZnO, but the PCE of PSCs increased from 7.01% (ZnO ETL) to 10.34% (graphene doped ZnO ETL). This study also showed an improvement of PV performance with 75% increasing of photon-to-current conversion efficiency after using graphene doped ZnO as an ETL layer in PSCs. In 2020, Chandrasekhar et al. also demonstrated the photovoltaic performance of PSCs by employing nitrogen-doped graphene (NG)/ZnO as an ETL [17]. This study varying the NG concentration from 0 to 1 wt% with an increment of 0.2 wt% in ZnO. The pristine ZnO are used as reference device. PCE are improved by increasing the concentration of NG from 0 wt% to 0.8 wt% and then decreased with 1.0 wt% of NG. PCE from 12.87% to 16.82% and exhibited a hysteresis free photovoltaic behaviour in comparison to pristine ZnO. Ahmed et al. prepare PSCs device with ZnO/graphene quantum dots (GQDs) used as an ETL in order to inspected the role of the ZnO/GQD ETL, they compare the different volume ratios of GQDs with 0.0%, 0.5%, 1.0%, 1.5%, 2.0%, and 2.5% and the 0% volume of GQD is marked as reference device [18]. As the result, all PV parameters were enhanced, together with an increment of the PCE from 10 % to 17.67%.

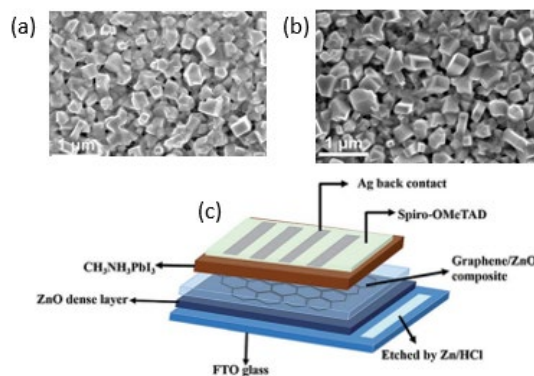


Figure 3. (a) SEM image of the pristine TiO₂, (b) graphene-doped TiO₂ [12], (c) structure for graphene doped ZnO as an ETL in PSCs [16]

In 2018, Zhu et al. investigate the effect of the graphene in the tin oxide (SnO_2) as an ETL on the photovoltaic performance [19]. The incorporation of graphene in the SnO_2 , improved the J_{sc} , FF , open circuit voltage (V_{oc}), and PCE. The enhancement are mainly attributed to the improved the electron extraction in the ETL layer and the efficient charge separation at the perovskite/ETL interfaces. The PCE increase from 17.01% to 18.11% with increase of graphene dopant. Besides that, Hong et al. have been improved efficiency in 2020 of PSCs by doping 5% of volume nitrogen-doped graphene oxide (NGO) in SnO_2 as an ETL [20]. The NGO acts as an oxidizing agent for SnO_2 . Based on the photovoltaic parameters, the greatest PCE was obtained at 16.54% with the doping of NGO compared with undoped SnO_2 with 15.36% of PCE. It is believed that the PCE of the PSCs with NGO in the SnO_2 layer were significantly higher due to the reduction of the trap states in the SnO_2 : NGO composite layer.

Many inorganic materials can be considered to be used as ETL including titanium dioxide (TiO_2), zinc oxide (ZnO), cadmium sulfide (CdS) and cadmium selenide (CdSe), and tin oxide (SnO_2). ZnO has a band gap similar to TiO_2 of 3.2 eV. The electron mobility of ZnO is $200 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, which is two orders of magnitude higher than that of TiO_2 [21]. Thus, it is expected that the used of ZnO -based as ETL in a PSCs could enhance the electron extraction and transport in the PSCs device. There are many synthesize methods and deposition techniques reported of ZnO thin films. Deghan et al.²² studied and compared several deposition techniques of ZnO through spin coating, spray pyrolysis and SILAR, and observed their performance as ETL in PSCs. They reported the ETL grown using spin coating technique demonstrated good performance compared to spray pyrolysis and SILAR technique since it produced the lowest bandgap of 3.27 eV and highest PCE of 7.0%. Spin coating method has more advantages such as efficiency, low cost, and ease of production. Other method was studied by Zhao et al. using solution processed and sputtered method [23]. The sputtered ZnO layer exhibited larger grain size, higher energy-level, better optical transmission and higher conductivity compared to solution processed technique. The device with the sputtered ETL had an excellent performance due to the improvement in the V_{oc} and FF . The increase in V_{oc} was probably due to the lower recombination rate and the larger surface area at the interface that contributed to an intense improvement in the thermochemical stability.

On the other hand, several sulfides and selenides material were also considered as inorganic material for ETL in PSCs. Uniform metal chalcogenide (MCs) film was deposited using a low-cost and scalable chemical bath deposition (CBD) method and achieve near 11% PCE [24]. They discovered that the annealing temperature process had no effect in improvement of devices efficiency. They concluded that metal chalcogenide can be a good ETL since it showed high stability, reproducibility and a better scalability for low temperature deposition, which this can be extended to be applied in flexible substrates. In addition, researcher from China introduced low-temperature processed PSCs using metal sulfide as electron transport layer [25]. In order to find the effect of metal sulfide on device performance, perovskite solar cells with and without electron transport layer were fabricated. Only 3.58% PCE achieved for the ETL free device. This study varying the different thickness of the metal sulfide layer with 30 nm, 50 nm and 100 nm. The layer of 30 nm was fabricated, a V_{oc} of 1.05 V, a J_{sc} of 16.14 mA/cm^2 and a FF of 0.66 were achieved, resulting in the champion PCE of 11.17%. From the photovoltaic performance reveal the presence of metal sulfide layer results in efficient collection and reduced recombination.

Recently, SnO_2 has received significant attention as an ETL for PSCs, and it is regarded as the most promising as TiO_2 . Several groups reported that SnO_2 displayed a band gap of 3.8 eV, and the electron mobility of $250 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, which both is higher than that of the TiO_2 or ZnO counterparts [21]. Faster electron mobility might improve the ETL's capacity for transporting electrons, while a wider band gap might prevent the ETL from absorbing photons with high energy, which might result in little current loss. In addition, SnO_2 nanoparticles have the advantages of possessing good anti-reflective properties and thermal stability [26]. Ren et al. studied the solution processed of pristine SnO_2 and Niobium (Nb) doped SnO_2 [27]. The result showed that the power conversion efficiency of PSCs based on SnO_2 doped Nb ETL was raised to 17.57% from 15.13% (pristine SnO_2). SnO_2 doped Nb ETL show an improved surface morphology, higher electron mobility, larger electrical conductivity, and enhanced electron extraction. Besides that, the method of deposition of SnO_2 also become one of the effect of device efficiency. PCEs estimated from the stable photocurrent are only 4.5% (sol-gel SnO_2) and 8.7% (hydrothermal SnO_2), respectively [28]. They also observed the increment of PCE of the PSCs to 18.8% after adding C60 to SnO_2 ETL. On the other hand, the UV/ozone (UVO) pre-treatment can increased the PCE of the device. A study show new method is the regrowth of the new SnO_2 nanocrystallines absorbed on the FTO or ITO surfaces introduced by UVO pretreatment [29]. PSCs devices based on their new SnO_2 ETL show high PCEs up to 20.5%, with a steady-state output of 20.1%.

2.1 Brief History Of TiO₂

TiO₂ is a well-known inorganic material that is commonly used as buffer layer in sensors and ETL in solar cells. TiO₂ nanoparticle has a bandgap of 3.0 to 3.2 eV and an electron mobility of approximately 1 cm²V⁻¹s⁻¹ [21]. There are numerous production methods available to create a good TiO₂ layer with the necessary parameter. TiO₂ has high electrical conductivity even before it is transformed into nanocrystallites. As a result, TiO₂ requires a high temperature process. TiO₂ compact layer is usually constructed on FTO substrate, which can withstand high temperatures of up to 700°C [30]. TiO₂ contains various phases, the most notable are anatase and rutile [31]. A three-dimensional (3D) crystal structure of anatase and rutile are shown in Figure 4. Both TiO₂ phases have tetragonal unit cell and crystal structures composed of TiO₆ octahedra. Anatase and rutile TiO₂ have band gaps of about 3.20 eV and 3.03 eV, respectively [32]. TiO₂-based nanostructures have been intensively studied in research and deployed in a range of applications over the last few decades. When a photon (with energy equal to or greater than the TiO₂ bandgap) impinges TiO₂ particles or film, electrons are triggered from the valance band to the conduction band, resulting in electron-hole pairs. The formed charge carriers migrate towards the surface and react with the adsorbed chemicals, thereby degrading the organic pollutants. The process shown in Figure 5.

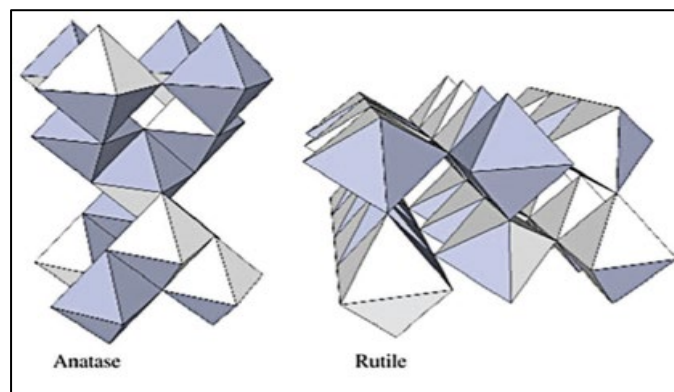


Figure 4. The three-dimensional representation of the crystal structure of anatase and rutile of TiO₂ [41]

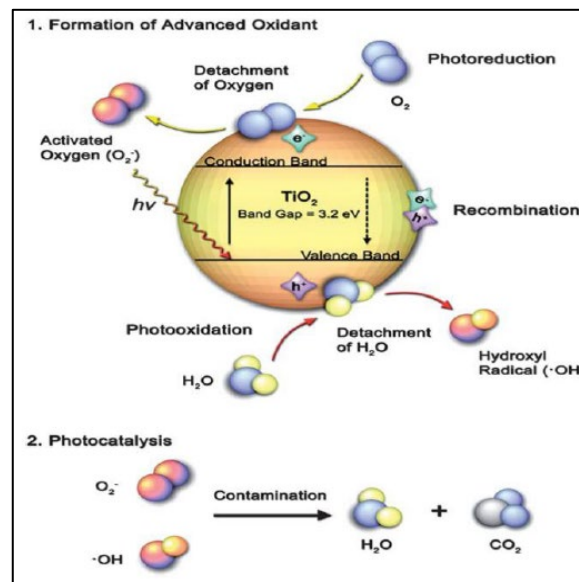


Figure 5. Photocatalysis process of TiO₂ [42]

3.0 TiO₂ AS THIN FILMS

TiO₂-based photocatalytic thin films and nanostructures commonly employed in a wide range of applications such as remediation, self-cleaning windows, water splitting, hydrogen release, and antibacterial material due to their interesting chemical, electrical, and optical properties [33-35]. TiO₂ thin

films can be applied to various type of substrates, and instead of bulk materials, TiO₂ thin films are used to aims for significant cost savings. In addition, the thin film catalysts can reduce the recombination of UV-activated electrons and holes, thereby increasing the quantum efficiency [36-37]. TiO₂ thin films at the nanoscale, enables distinct mechanical, chemical, and physical surface functions, such as increased surface area and improved photocatalytic activity [38]. TiO₂ in thin film also exhibit high stability in aqueous solutions, no photocorrosion under bandgap illumination, as well as exceptional surface properties [39]. Another important characteristic of TiO₂ films related to photo-generation of electron hole pairs is that it can improve the surface with photo-induced hydrophilicity [40].

3.1 Deposition Techniques For TiO₂ Thin Film

TiO₂ thin films have been synthesized by several sophisticated techniques such as chemical vapor deposition (CVD) [38,50], metal organic chemical vapour deposition (MOCVD) [38,43], hydrothermal synthesis [44,55], sputtering [38,45], liquid phase deposition (LPD) [38,46], electrophoretic deposition [38,47], sol-gel [38,57-61,64,66], atomic layer deposition (ALD) [38,48], doctor-blade [38,53,54], etc. The synthesis methods and processing conditions have significant impact on the physical properties of the materials. Since the microstructure and physical properties of TiO₂ affect its photocatalytic activity, the proper choice of deposition technique is important for a good results. Table 1 shows the summary of TiO₂ properties thin film deposited by various technique.

Table 1: Common methods for TiO₂ coating and their advantages and disadvantages

Method	Description	Advantages	Disadvantages	References
Sputtering	Use the energy of a plasma (partially ionized gas) on the surface of a target (cathode), to pull the atoms of the desired material one by one and deposit them on the substrate (Pressure of 0.8 Pa)	<ul style="list-style-type: none"> • Suitable for insulator and semiconductor substrate • Good film adhesion 	<ul style="list-style-type: none"> • Risk of substrate damage due to ionic bombardment • Relatively low coating rates 	[38, 45, 49]
CVD	A thin film is formed on a heated substrate from a gaseous phase in a closed chamber at a relatively higher temperature. The process which vapour phase materials are condensed to produce solid phase material	<ul style="list-style-type: none"> • Thickness of the layer can be controlled • Produce uniform films at low or high rate • Compatibility with good adhesion 	<ul style="list-style-type: none"> • High temperature • High cost • High reaction temperature • Low deposition rate • Presence of corrosive gases 	[38, 44, 50-51]
Sol-gel	The process is called wet chemical method for the synthesis of various nanostructures. In this method, the molecular precursor is dissolved in water or alcohol and converted to gel by heating, stirring and hydrolysis /alcoholysis until the solution become sol-gel phase	<ul style="list-style-type: none"> • Simple and easy • Low cost • Reliability and reproducibility • Suitable for any type of substrate • Full coating on substrate • low processing temperature 	<ul style="list-style-type: none"> • Required long processing time • Not suitable for thick layer • Notable to control and measure thickness during deposition 	[38, 44, 57-61, 64, 66]

Method	Description	Advantages	Disadvantages	References
Doctor-blade	The slurry is poured on a substrate, and a blade applies unidirectional shear force to the substrate	<ul style="list-style-type: none"> • Simplest, easy control of film thickness • Suitable for mass production • Low cost 	<ul style="list-style-type: none"> • Slow evaporation • Not suitable for low thickness 	[38, 53-54]
Hydro-thermal	Comprises a variety of methods for crystallizing substances from high-temperature aqueous solutions at high vapour pressures	<ul style="list-style-type: none"> • Simple operation technique • Ability to grow large, high-quality crystals 	<ul style="list-style-type: none"> • High cost • Required high temperature • It is impossible observing the crystal grow 	[38, 44-55]

3.2 TiO₂ Grown Using Sol-Gel Method

The sol-gel process has become a popular method for preparing TiO₂ films due to its facile and matured application. The sol-gel method also has become one of the most popular approaches due to its effectiveness, homogeneity and dependability [38]. The sol-gel coating can be done at room temperature without the use of any expensive equipment [38]. The hydrolysis and polycondensation processes of the precursors, which are mainly metal organic compounds or inorganic metal salts, result in a colloidal suspension (a solution). Following polymerization and subsequent solvent loss, the liquid transforms into a solid gel phase. The sol-gel route combined with spin coating has been widely applied to coat metallic film in different substrates such as glass, silicon wafer, polyethylene terephthalate (PET), ceramic, and crystal substrate. By controlling the sol-to-gel transition and thereby the sol viscosity, a variety of shapes with desired porosity and texture can be obtained [56]. Sol-gel method allows to control over the porosity, texture, and chemical composition of deposited materials. However, in order to prepare films with controlled crystal size and texture, optimum conditions should be maintained for controlling the precursor reactivity [38].

You et al. employed a low temperature solution processes TiO₂ for ETL on two type of substrates a glass substrate known as rigid cell and flexible polymer substrate (PET) named as flexible cell [57]. The solution was spin coated on both of the substrates at 3000 rpm for 30 s and heat treated on a hot plate at 100°C for 30 min. The heat-treatment ensures the following orthogonal processability of perovskite solution. The rigid cell exhibited 18.2 % PCE under sun condition of AM1.5 G 100 mW/cm² and the flexible cell has 15.8% PCE. Furthermore, the flexible perovskite solar cells exhibited good mechanical stability against repeated bending, so it maintained ~95% of its initial PCE after 1000 repeated bending cycles. Khan et al. studied the structural, electrical and optical properties of multilayer TiO₂ thin films deposited by sol-gel spin coating [58]. They presented the comparisons among the stacked layers of TiO₂ thin films. After homogenous solution of TiO₂ is prepared, it was spin coated at 2400 rpm for 30 second per layer. The experiment was repeated for the multilayer of the films. As the results, XRD confirms the anatase phase of TiO₂. The resistivity decreased with the increasing number of layer. Optical properties of the film showed high transmittance in the visible region and the optical band gap decreased by increasing the layers due to the formation of sub valence band in the forbidden band of TiO₂. El Haimour et al. synthesized TiO₂ using sol-gel process using butanol as solvent and acetic acid as catalyst [59]. They studied the effect of thickness on the morphological, structural and optical properties was investigated, as well as their applicability as electron transporting layer for planar PSCs. The distribution histograms of the grain sizes show a relatively narrow peak for all thicknesses, indicating that the films are morphologically uniform. XRD confirms the anatase phase of TiO₂ which grows mainly along (1 0 1) direction. Regardless of thicknesses, all films have a similar peak shape in the absorbance spectra graph. The results showed an improvement in charge transport and electron extraction behavior using the 50 nm TiO₂ layer, and thus a rise in the devices power conversion efficiency with 13.8% PCE.

3.3 TiO₂ Thin Film

In this section, only pristine TiO₂ without any material doping used as an ETL for PSCs devices. Tahir et al. has been synthesized TiO₂ using sol-gel and spin coating on the glass substrate [60]. In order to prepare

the solution, 50 mL ethanol was stirred for 15 minutes in 5 mL of acetic acid, the 6.3 mL of titanium tetraisopropoxide (TTIP) add drop by drop and continuous stirring for another 15 minutes. The transparent sol solution was aged for 24 hours. Next, the sol-gel was spin coating with speed of 3000 rpm and spin time 15 seconds. As the result Tahir et al. concluded that sol-gel coating technique employed excellent result better than any other techniques [60]. The thin films are uniform, crack free and good coverage of glass substrate with a spherical shape. Sławek et al. studied the influence of thickness of compact TiO₂ as an ETL [61]. The thickness was controlled with the different spin coating speed. The result show optimal thickness for the studied was 19.5 nm, resulting in the highest PCE of 13.6%. From the results, shows that the thickness of the compact TiO₂ ETL clearly affects the performance of MAPbI₃ PSCs. Furthermore, they noticed a clear gradual decrease in PCE from 13.6% to 7.3% along with the thickening of the compact TiO₂ ETL. Mandati et al. has been compared two different deposition methods of TiO₂ which are spin coating and bar coated [62]. For the bar coated technique, the precursor was dropped at the interface of bar and substrate. The wire wounded bar was then moved gradually to spread the precursor solution followed by drying the wet film in the air. As the result, the PSCs grown on bar coated ETL have exhibited a power conversion efficiency of 12.1% while the ones on spin coated ETL showed PCE of 11.5%.

3.4 Doped TiO₂ Thin Film

The doping process of TiO₂ is an essential method for overcoming the rapid recombination of photogenerated charge carriers. The doping method can reduce the rate of electron-hole pair recombination. Below are the several study of doped TiO₂ as an ETL with various material such as graphene, Zinc (Zn), Tin (Sn), and Magnesium (Mg). Dadashbeik et al. designed a simulation technique using a three-dimensional (3D) finite element method (FEM) to comparing PCE of PSCs on graphene and TiO₂ /graphene as an ETL [63]. As the result, graphene as an ETL has PCE of 16.03% but after proposed TiO₂ /graphene as an ETL, the PCE increase to 17.8%. This is due to the increased in the mobility of carriers. Ebrahimi et al. synthesized with different amount of volume of graphene quantum dots (GQD) to the mesoscopic TiO₂ using spin coating technique [64]. The results demonstrated that 2.5% volume of graphene showed the best PCE with 14.36% approximately 50% improvement compared to the un-doped PSCs has only PCE of 9.55%. This work clearly shows that doping of GQDs as a modifier to the TiO₂ layer can be a beneficial approach to improve the PSCs's performance and stability. Liu X et al. deposited Zn-doped TiO₂ as an ETL using low temperature solution processed route [65]. The different volume of Zn has been investigated. They found that Zn-doped TiO₂ films possess less trap-state density and better conductivity, compared to undoped thin films, which contributes to the promotion of short circuit current. The best PCE was 17.60% for the 4.5% volume of Zn. The outstanding performance using the Zn-doped TiO₂ ETL originates from high electrical conductivity, low trap state density, and enhanced electron extraction.

In order to enhance the planar structure of PSCs, Cai et al. using the Sn-doped TiO₂ as an ETL with low temperature fabrication procedure (below 100 °C) [66]. They compared with pristine TiO₂ (PCE =13.30%) and the Sn-doped TiO₂ (PCE = 15%-17%) which was almost 29.3% improvement. This result demonstrated that the Sn-doped TiO₂ is more efficient for photogenerated electron extraction and transport, showing lower trap-state density and higher conductivity. Arshsad et al. prepared Mg-doped TiO₂ as an ETL to enhance the optical and morphological properties of the layer and the interface [67]. They find that Mg doped in mesoporous TiO₂, was significantly improved thereby providing an interface for the growth of absorber layer. The best PCE was with 3 wt% Mg doped TiO₂ with PCE of 14.65%.

4.0 CONCLUSION

ETL can be an important factor in the fabrication of PSCs that guarantees the PSCs's excellent efficiency. This review cover the common materials for ETL, the list of deposition techniques of the ETL and device performance when doped TiO₂ are presented. This included the improvements to the electric structure and intrinsic properties, enhancements to electrical conductivity by boosting carrier density and mobility, and improvements to bandgap alignments, interface trap density, and distribution. Sol-gel technology has recently been used to create both pure TiO₂ films and modified or doped TiO₂ films for photocatalytic dye degradation. Although TiO₂ have several drawbacks such as its poor ability to absorb solar irradiation, its wide bandgap (3.2 eV) limits the use of visible light as the light source and the adsorption capacity of TiO₂ is relatively low. Continued research in this field is expected to lead to a better understanding and indicate a way to overcome existing limitations.

5.0 CONFLICT OF INTEREST

The authors declare no conflicts of interest.

6.0 AUTHORS CONTRIBUTION

Azmi, A. H. (Validation; Investigation; Writing - origin draft)

Abdul manaf, N. A. (Conception; Funding acquisition; Supervision)

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